



In re Application of:

Subhash Narang et al.

Art Unit: 1762

Serial No: 09/762,881

Examiner: Padgett, Marianne L.

Filed: 03/27/2001

Confirmation number: 1944

Docket number: 360.07-US1 (SRI-101)

For: Printing of Electronic Circuits and Components

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Sir:

Applicant replies to the Office Action mailed 7/02/2004 as follows: